

SYNPLEX

A task-parallel scheme for the revised simplex method

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Overview

- The (standard and revised) simplex method for linear programming

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- Approaches to parallelising the simplex method

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- Approaches to parallelising the simplex method
- SYNPLEX

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- The (standard and revised) simplex method for linear programming
- Approaches to parallelising the simplex method
- SYNPLEX
- Results and conclusions

Solving LP problems

$$\begin{array}{ll} \text{minimize} & f = \mathbf{c}^T \mathbf{x} \\ \text{subject to} & A\mathbf{x} = \mathbf{b} \\ & \mathbf{x} \geq \mathbf{0} \\ \text{where} & \mathbf{x} \in \mathbb{R}^n \quad \text{and} \quad \mathbf{b} \in \mathbb{R}^m \end{array}$$

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- At any vertex the variables may be partitioned into index sets
 - $\mathcal{B}$ of m basic variables $\mathbf{x}_B \geq \mathbf{0}$
 - $\mathcal{N}$ of $n - m$ nonbasic variables $\mathbf{x}_N = \mathbf{0}$

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 - $\mathcal{N}$ of $n - m$ nonbasic variables $\mathbf{x}_N = \mathbf{0}$
- Components of $\mathbf{c}$ and columns of A are
 - the basic costs $\mathbf{c}_B$ and basis matrix B
 - the non-basic costs $\mathbf{c}_N$ and matrix N

Reduced LP problem

At any vertex the original problem is

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Eliminate $\mathbf{x}_B$ from the objective to give

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where $\hat{\mathbf{b}} = B^{-1}\mathbf{b}$, $\hat{N} = B^{-1}N$, $\hat{f} = \mathbf{c}_B^T \hat{\mathbf{b}}$ and $\hat{\mathbf{c}}_N$ is the vector of **reduced costs**

$$\hat{\mathbf{c}}_N^T = \mathbf{c}_N^T - \mathbf{c}_B^T \hat{N}$$

The standard simplex method

	$\mathcal{N}$	$\mathcal{B}$	RHS
1 $\vdots$ m	$\hat{N}$	I	$\hat{\mathbf{b}}$
0	$\hat{\mathbf{c}}_N^T$	$\mathbf{0}^T$	$-\hat{f}$

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- Exchange indices p' and q between sets $\mathcal{B}$ and $\mathcal{N}$
- Update tableau corresponding to this **basis change**

The standard simplex method (cont.)

Advantages:

- Easy to understand
- Simple to implement

The standard simplex method (cont.)

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Disadvantages:

- Expensive: the matrix $\hat{N}$ 'usually' treated as full
 - Storage requirement: $O(mn)$ memory locations
 - Computation requirement: $O(mn)$ floating point operations per iteration
- Numerically unstable

Revised simplex method

Given $\hat{\mathbf{c}}_N$, $\hat{\mathbf{b}}$ and a representation of $\mathbf{B}^{-1}$, repeat

CHUZC: Scan the reduced costs $\hat{\mathbf{c}}_N$ for a good candidate q to enter the basis

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If (growth in factors) then

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Factored representation of B^{-1}

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Loop {minor iterations}

CHUZO_MI: Scan $\hat{\mathbf{c}}_Q$ for a good candidate q to enter the basis

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UPDATE_MI: Update $\mathcal{Q} := \mathcal{Q} \setminus \{q\}$; $\hat{\mathbf{b}} := \hat{\mathbf{b}} - \alpha \hat{\mathbf{a}}_q$; $\hat{\mathbf{a}}_j$ and $\hat{\mathbf{c}}_j$, $\forall j \in \mathcal{Q}$

End loop {minor iterations}

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 End loop {minor iterations}
 For {each basis change} do
 BTRAN: Form $\boldsymbol{\pi}^T = \mathbf{e}_p^T B^{-1}$
 PRICE: Form pivotal row $\hat{\mathbf{a}}_p^T = \boldsymbol{\pi}^T N$ and update $\hat{\mathbf{c}}_N := \hat{\mathbf{c}}_N - \hat{c}_q \hat{\mathbf{a}}_p^T$
 If {growth in factors} then
 INVERT: Form a new representation of B^{-1}
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Advantages:

- Offers scope for task parallelism

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- Exploit **data parallelism**
Use several processors simultaneously to perform a single operation

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How?

- Exploit **data parallelism**
Use several processors simultaneously to perform a single operation
- Exploit **task parallelism**
Perform more than one operation simultaneously using several processors

Parallelising simplex computational components

Component

Properties

Scope for data parallelism

Parallelising simplex computational components

Component	Properties	Scope for data parallelism
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PRICE	Matrix vector product	Immediate
INVERT	Searches through B_0 and (half-)FTRANs	Little (traditionally)

Past approaches

Standard simplex method

- Good parallel efficiency achieved

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Data parallel revised simplex method

- Only the immediate parallelism in PRICE has been exploited
- Significant speed-up only obtained when $n \gg m$ so PRICE dominates
For such problems an efficient serial solver uses partial pricing so PRICE no longer dominates

Data/task parallel revised simplex method (with multiple pricing)

Wunderling (1996)

- Parallel (except for INVERT) for only two processors
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- Fully task parallel (inefficient) variant of the revised simplex method
- Speed-up (on Cray T3D) of up to 5 on modest Netlib problems

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PARSMI: Hall and McKinnon (1996)

- Fully task parallel revised simplex method with multiple pricing
- Speed-up (on Cray T3D) of between 1.7 and 1.9 on modest Netlib problems

SYNPLEX, a task-parallel scheme for the revised simplex method



(Some) ASYNPLEX and PARSMI limitations

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- Numerically unstable—due to overlapping INVERT with basis changes
- Reduced costs always out-of-date—more iterations and wasted FTRANs
- Significant communication overhead

SYNPLEX

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SYNPLEX

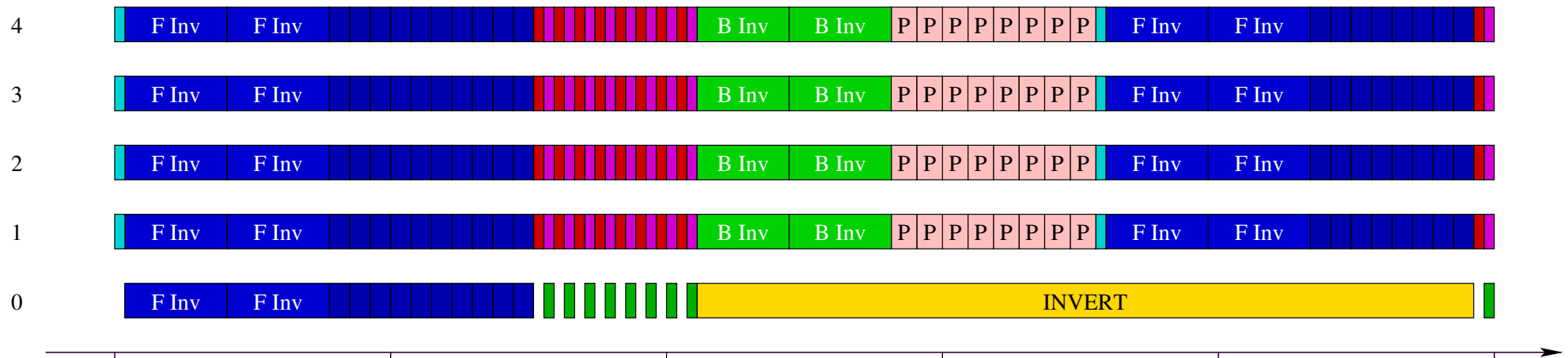
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- Target platform: shared memory Sun Fire E15k (OpenMP)

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Data parallelism

When using $1 + p$ processors

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 - FTRAN for UPDATE etas
 - CHUZR
 - UPDATE tableau in minor iterations
 - UPDATE RHS
- Columns distributed over p processors

Data parallelism

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 - FTRAN for UPDATE etas
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 - UPDATE tableau in minor iterations
 - UPDATE RHS
- Columns distributed over p processors for data parallel
 - PRICE
 - CHUZZ

Data location challenge

- B_0 factored serially on one processor

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- B_0 factored serially on one processor
- Factors used serially on all processors to solve linear systems
- Each solution used for data parallel operations over all processors

Practical implementation

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 - Task parallel operations allocated according to processor activity
 - Enables different computational components to overlap
- Prevent different processors from writing to consecutive components
 - Insert “padding” between row partitions: implemented
 - Insert “padding” between column partitions: not yet implemented

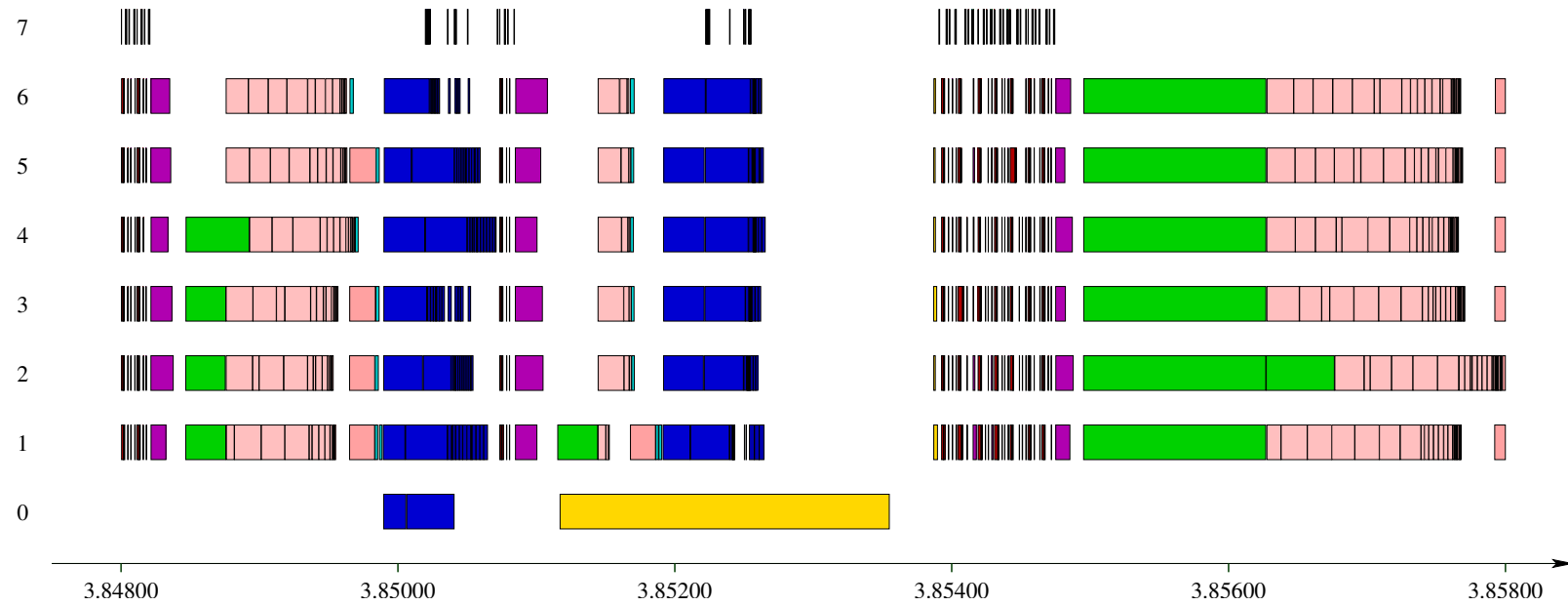
Results

Model	Rows	Columns	1 Processor	Speed-up	
			CPU (s)	4 processors	8 processors
cre-a	3517	4067	5.76	1.16	1.83
25fv47	822	1571	8.78	1.54	1.99
greenbea	2393	5405	29.22	-	2.30
ken-11	14695	21349	41.26	1.40	2.52
stocfor3	16676	15695	98.44	1.50	2.76
pds-06	9882	28655	138.84	1.58	3.05



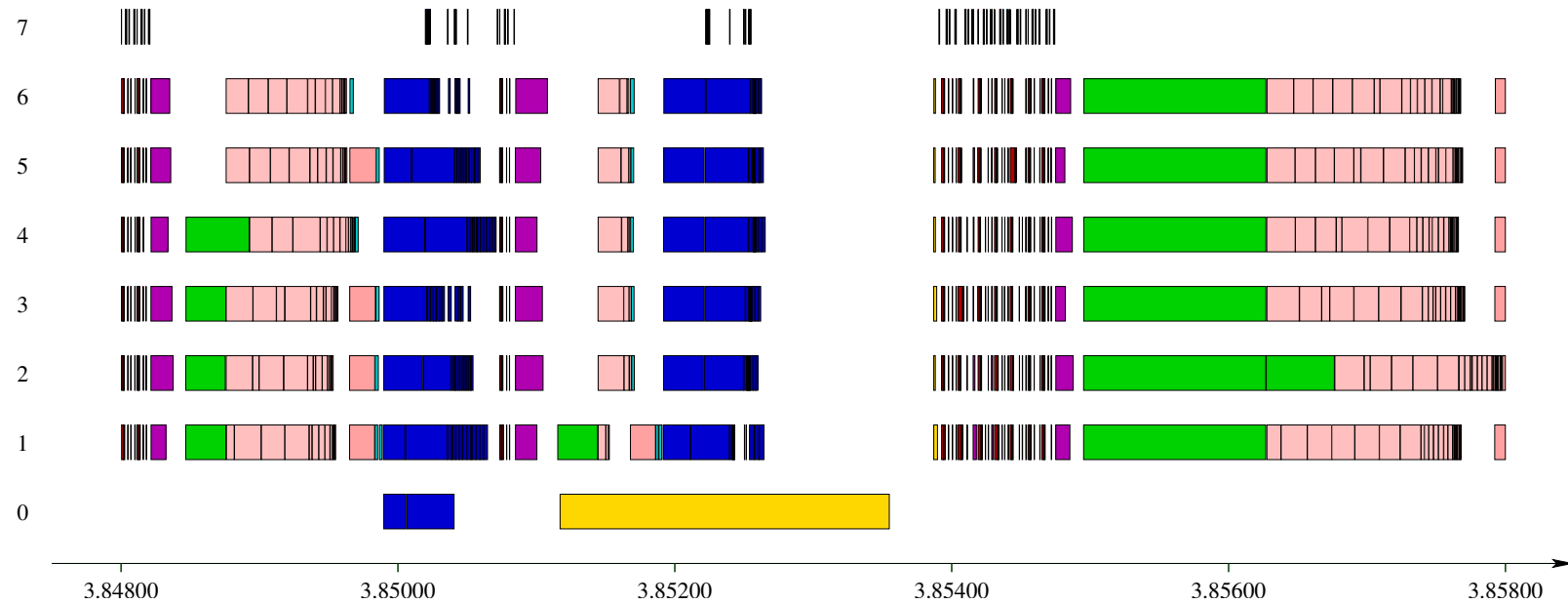
Poor performance: cre-a

Least speed-up (1.83) on 8 processors



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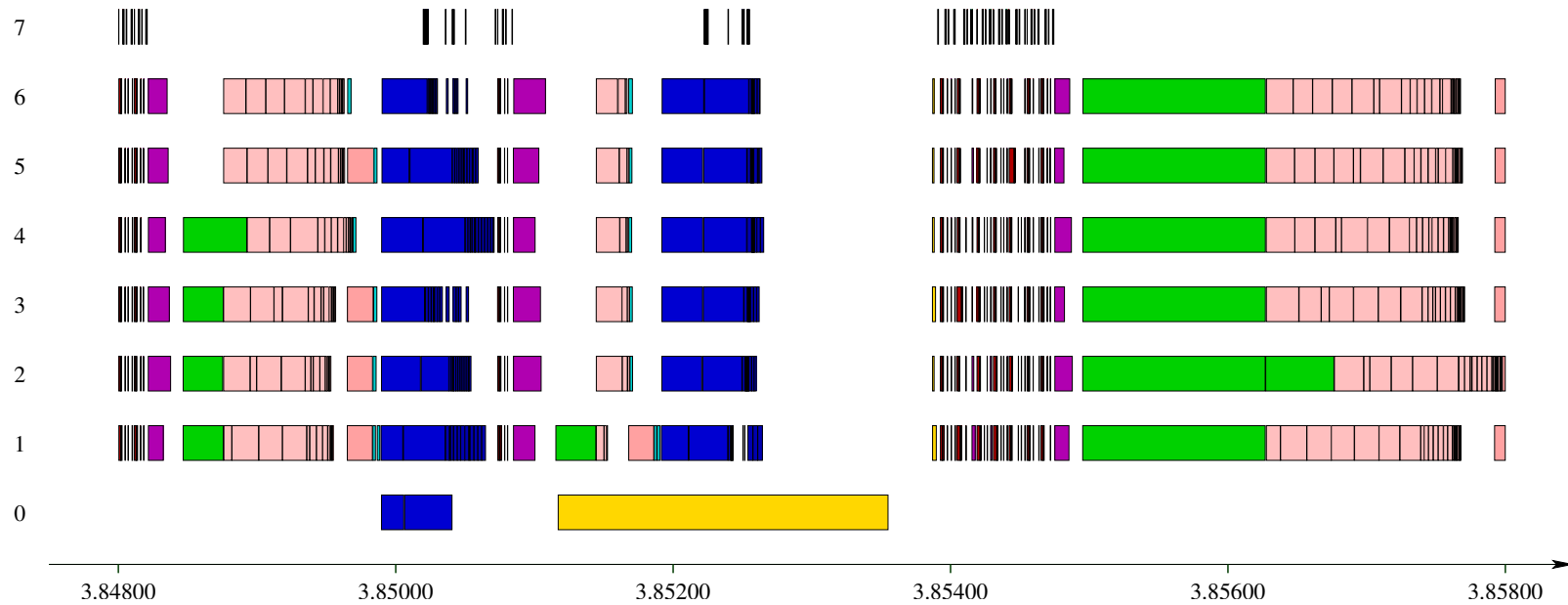
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Operation	Slow-down in total time	Overall speed-up
Inv-FTRAN	3.29	-

Poor performance: cre-a

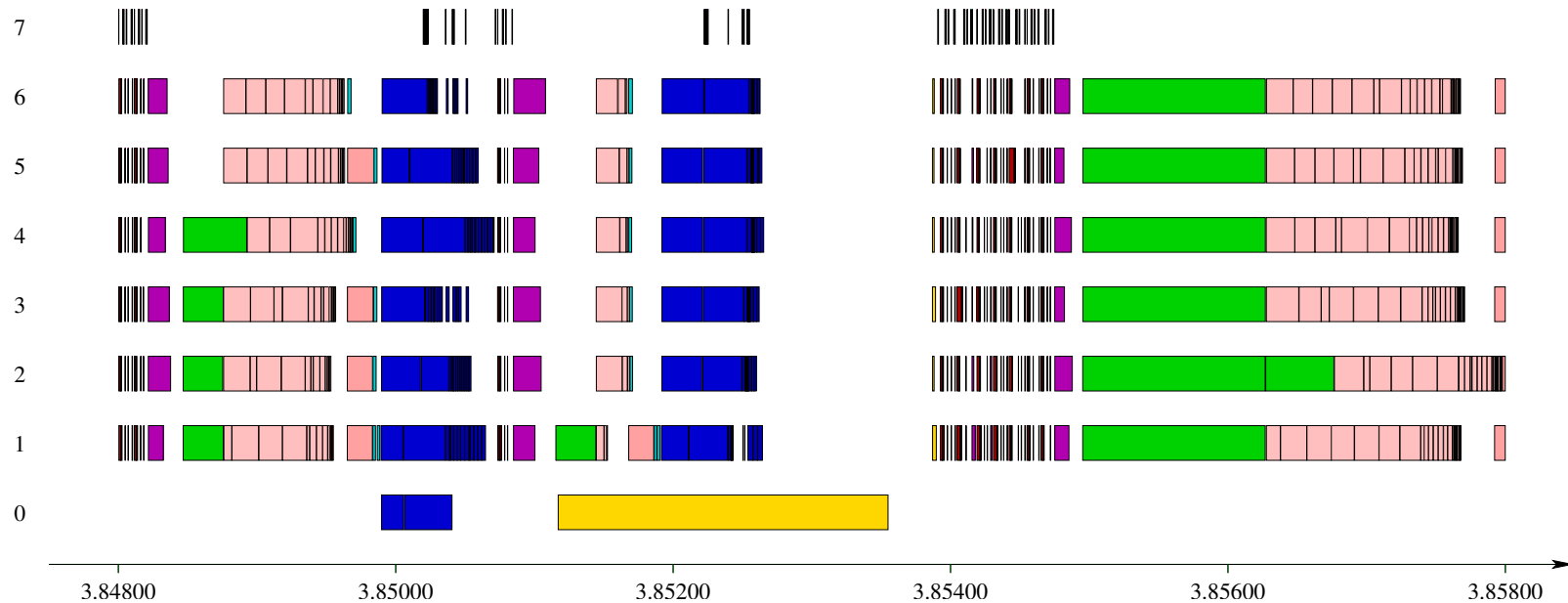
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Operation	Slow-down in total time	Overall speed-up
Inv-FTRAN	3.29	-
Inv-BTRAN	2.11	-

Poor performance: cre-a

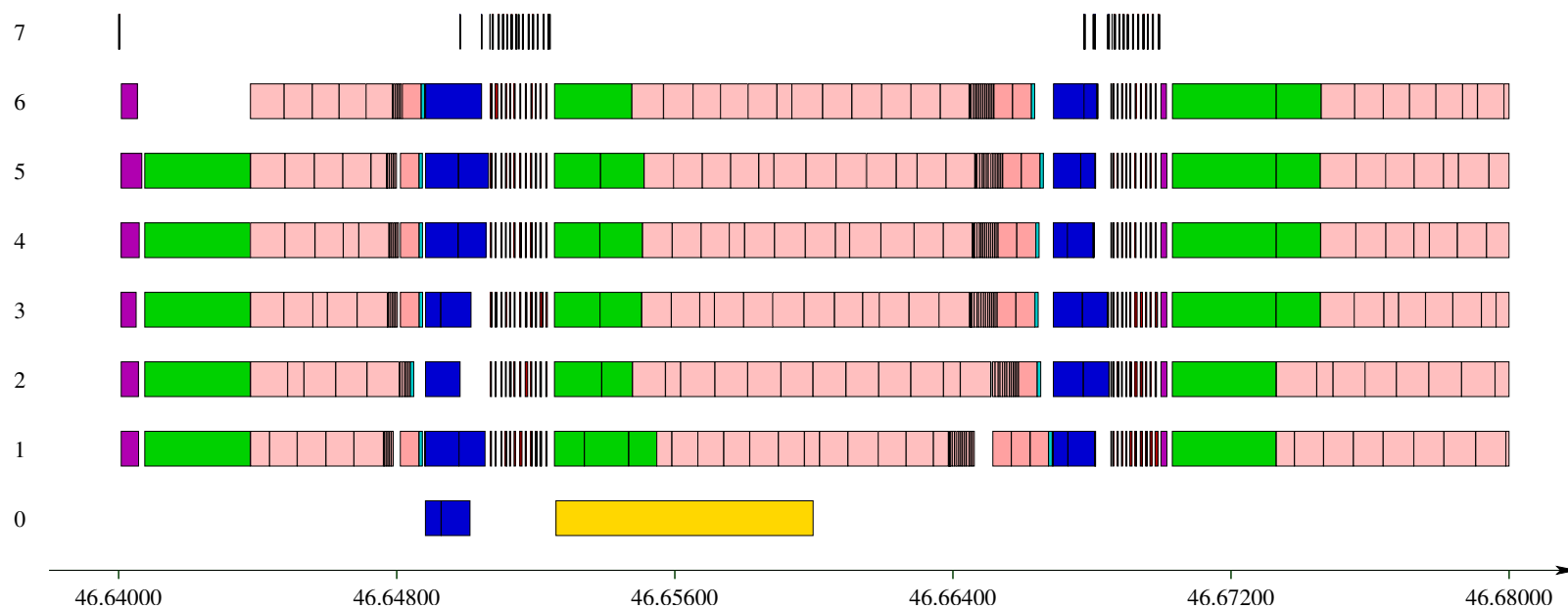
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Operation	Slow-down in total time	Overall speed-up
Inv-FTRAN	3.29	-
Inv-BTRAN	2.11	-
PRICE	2.94	2.04

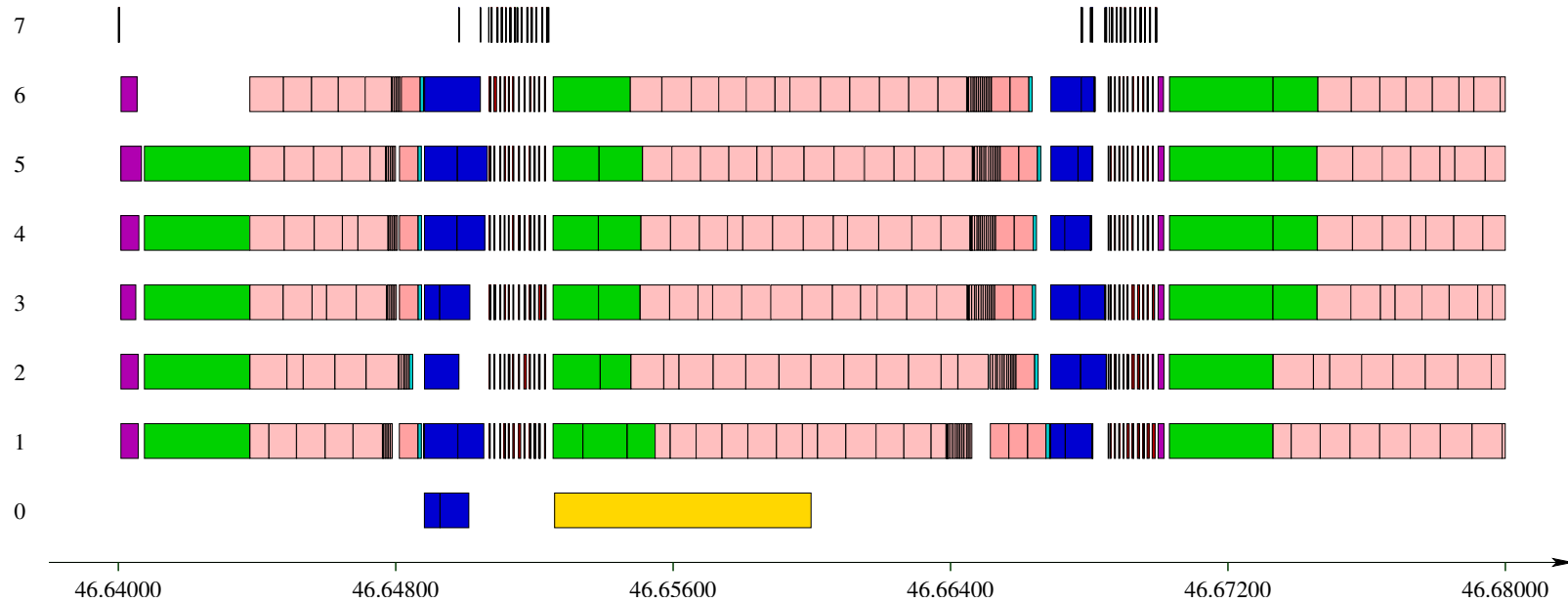
Fair performance: pds-06

Best speed-up (3.05) on 8 processors



Fair performance: pds-06

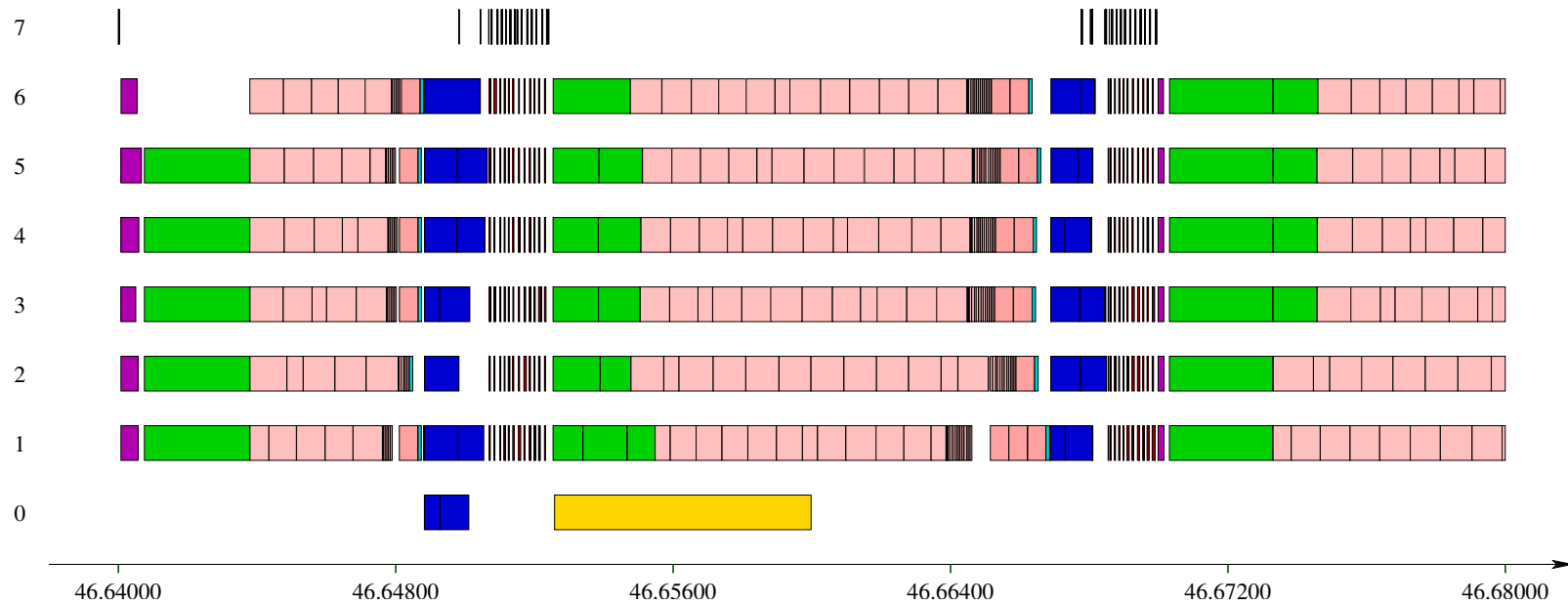
Best speed-up (3.05) on 8 processors



Operation	Slow-down in total time	Overall speed-up
Inv-FTRAN	2.02	-

Fair performance: pds-06

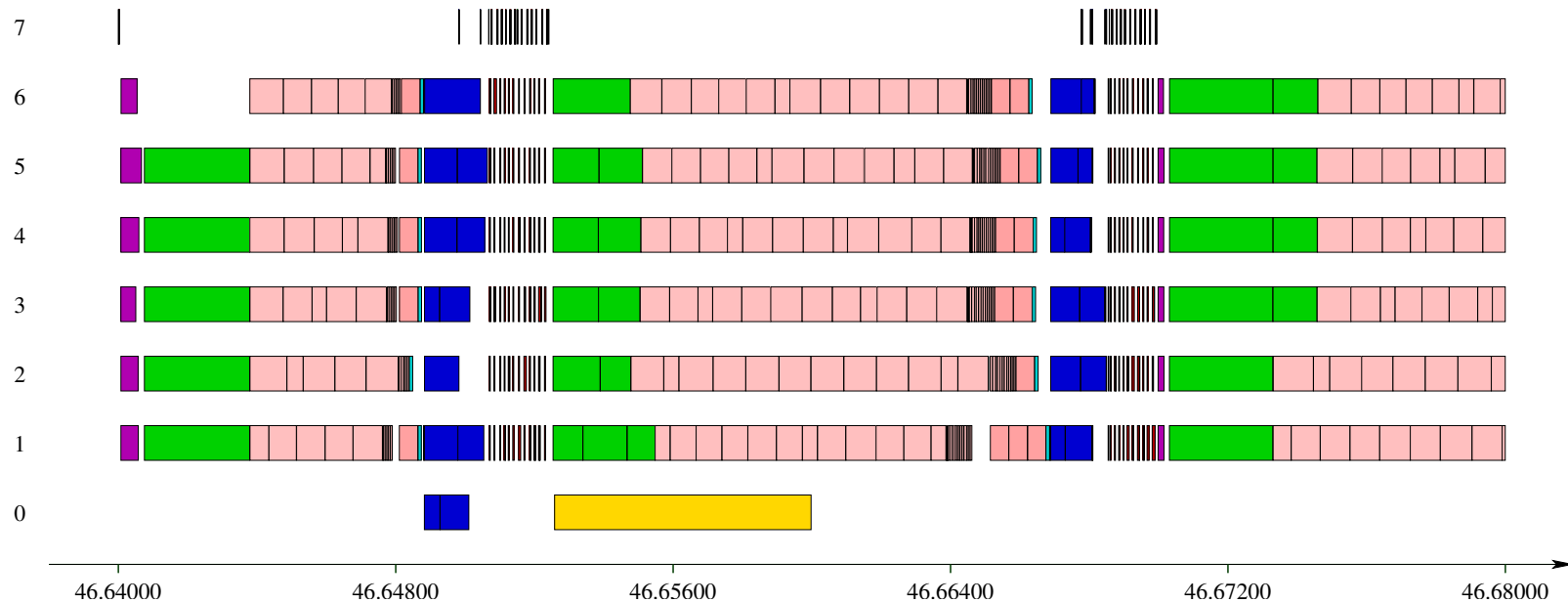
Best speed-up (3.05) on 8 processors



Operation	Slow-down in total time	Overall speed-up
Inv-FTRAN	2.02	-
Inv-BTRAN	1.79	-

Fair performance: pds-06

Best speed-up (3.05) on 8 processors



Operation	Slow-down in total time	Overall speed-up
Inv-FTRAN	2.02	-
Inv-BTRAN	1.79	-
PRICE	1.69	3.55

Conclusions

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 - Wasted FTRANs
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Future prospects:

- Pure data parallel revised simplex *without* multiple pricing

Bibliography

Paper: <http://www.maths.ed.ac.uk/hall/ParSimplex>
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